EYE PATTERN DISPLAY SYSTEM

Patent Number: JP62003544

Publication date: 1987-01-09

Inventor(s):   YOSHIDA ATSUSHI

Applicant(s):   NEC CORP

Requested Patent: ☐ JP62003544

Application Number: JP19850141528 19850629

Priority Number(s):

IPC Classification: H04L27/00

EC Classification:

Equivalents: JP1747726C, JP4031452B

Abstract

PURPOSE:To obtain an eye pattern being not unnatural without using a screen storage type oscilloscope, a large RAM and a large software by writing a next data to an address written in at first when data are written in the entire area.
CONSTITUTION:A RAM bank 13 has 512-byte X 2 constitution and the input/ output is separated. An output of a 1/511 frequency divider 12 is a pulse in 0.2kHz and reception signal point data X, Y are written in the RAM bank 13 once per 5ms by giving the said pulse to a WR input of the RAM bank 13. Since a clock in 102.2kHz is given to an RD input of the RAM bank 13, data of addresses 000, 001, 002... are read sequentially from the RAM bank 13 at each 9.78mus and sent to D/A converters 14, 15. Output voltages (x, y) of the D/A converters 14, 15 are given to, e.g., an X-Y oxcilloscope, on which an eye pattern is displayed.